

# GCSE Subject Level Conditions and Requirements for Electronics April 2016

Ofqual/16/5944



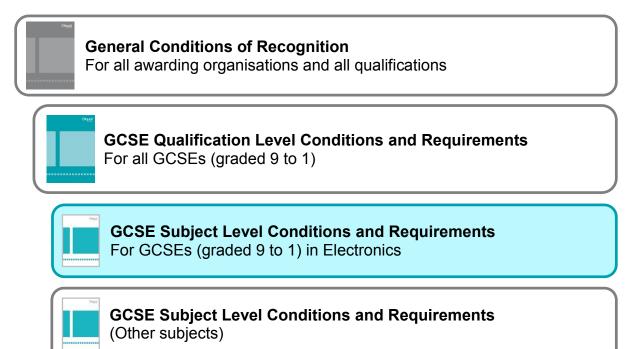
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# Introduction

# About this document

This document (highlighted in the figure below) is part of a suite of documents which sets out the regulatory requirements for awarding organisations offering GCSE qualifications (graded from 9 to 1).



We have developed these requirements with the intention that GCSE qualifications (graded from 9 to 1) should fulfil the following purposes:

- To provide evidence of students' achievements against demanding and fulfilling content;
- To provide a strong foundation for further academic and vocational study and for employment; and
- To provide (if required) a basis for schools and colleges to be held accountable for the performance of all of their students.

# Requirements set out in this document

This document sets out the GCSE Subject Level Conditions for Electronics. These conditions will come into effect at 12.01pm on Thursday 21 April 2016 for all GCSE qualifications (graded from 9 to 1) in Electronics.

It also sets out our requirements in relation to:

- assessment objectives awarding organisations must comply with these requirements under Condition GCSE(Electronics)1.2; and
- assessment awarding organisations must comply with these requirements under Condition GCSE(Electronics)2.3.

Appendix 1 reproduces the requirements in relation to subject content for GCSE Electronics<sup>1</sup>, as published by the Department for Education. Awarding organisations must comply with these requirements under Condition GCSE(Electronics)1.1.

With respect to GCSE qualifications (graded from 9 to 1) in Electronics, awarding organisations must also comply with:

- our General Conditions of Recognition,<sup>2</sup> which apply to all awarding organisations and qualifications; and
- our GCSE Qualification Level Conditions;<sup>3</sup> and
- all relevant Regulatory Documents.<sup>4</sup>

With respect to GCSE qualifications graded from A\* to G, awarding organisations must continue to comply with the General Conditions of Recognition, and the relevant Regulatory Documents.

## **Summary of requirements**

| Subject Level Conditions |                                      |
|--------------------------|--------------------------------------|
| GCSE(Electronics)1       | Compliance with content requirements |
| GCSE(Electronics)2       | Assessment                           |

#### Assessment Objectives

Assessment Objectives - GCSE Qualifications in Electronics

<sup>3</sup> <u>www.gov.uk/government/publications/gcse-9-to-1-qualification-level-conditions</u>

<sup>4</sup> <u>www.gov.uk/guidance/regulatory-document-list</u>

<sup>&</sup>lt;sup>1</sup> <u>www.gov.uk/government/publications/gcse-electronics</u>

<sup>&</sup>lt;sup>2</sup> <u>www.gov.uk/government/publications/general-conditions-of-recognition</u>

Assessment requirements

Assessment requirements - GCSE Qualifications in Electronics

Appendix 1 – Subject content (published by Department for Education)

GCSE Electronics: subject content

# **Subject Level Conditions**

# **GCSE Subject Level Conditions for Electronics**

Condition GCSE(Electronics)1 **Compliance with content requirements** 

- GCSE (Electronics)1.1 In respect of each GCSE Qualification in Electronics which it makes available, or proposes to make available, an awarding organisation must
  - (a) comply with the requirements relating to that qualification set out in the document published by the Secretary of State entitled 'Electronics GCSE subject content'<sup>5</sup>, document reference DFE-00039-2016,
  - (b) have regard to any recommendations or guidelines relating to that qualification set out in that document, and
  - (c) interpret that document in accordance with any requirements, and having regard to any guidance, which may be published by Ofqual and revised from time to time.
- GCSE (Electronics)1.2 In respect of each GCSE Qualification in Electronics which it makes available, or proposes to make available, an awarding organisation must comply with any requirements, and have regard to any guidance, relating to the objectives to be met by any assessment for that qualification which may be published by Ofqual and revised from time to time.

<sup>&</sup>lt;sup>5</sup> www.gov.uk/government/publications/gcse-electronics

| Condition<br>GCSE(Electronics)2 | Assessment  |
|---------------------------------|---|
| GCSE<br>(Electronics)2.1        | Condition GCSE4.1 does not apply to any GCSE Qualification<br>in Electronics which an awarding organisation makes available<br>or proposes to make available.   |
| GCSE<br>(Electronics)2.2        | In respect of the total marks available for a GCSE Qualification<br>in Electronics which it makes available, an awarding<br>organisation must ensure that –   |
|                                 | <ul> <li>(a) 80 per cent of those marks are made available through<br/>Assessments by Examination, and</li> </ul>   |
|                                 | (b) 20 per cent of those marks are made available through assessments that are not Assessments by Examination.  |
| GCSE(Electronics)2.3            | An awarding organisation must ensure that in respect of each<br>assessment for a GCSE Qualification in Electronics which it<br>makes available it complies with any requirements, and has<br>regard to any guidance, which may be published by Ofqual<br>and revised from time to time. |

# **Assessment objectives**

# Assessment objectives – GCSE Qualifications in Electronics

Condition GCSE(Electronics)1.2 allows us to specify requirements relating to the objectives to be met by any assessment for GCSE Qualifications in Electronics.

The assessment objectives set out below constitute requirements for the purposes of Condition GCSE(Electronics)1.2. Awarding organisations must comply with these requirements in relation to all GCSE Qualifications in Electronics they make available.

|     | Objective  | Weighting |
|-----|--|-----------|
| AO1 | Demonstrate knowledge and understanding of the ideas, techniques and procedures of electronics       | 35%       |
| AO2 | Apply knowledge and understanding of the ideas, techniques and procedures of electronics             | 35%       |
| AO3 | Analyse problems and design, build, test and evaluate electronic systems to address identified needs | 30%       |

# **Assessment requirements**

## Assessment requirements – GCSE Qualifications in Electronics

Condition GCSE(Electronics)2.3 allows us to specify requirements in relation to assessments for GCSE Qualifications in Electronics.

We set out below our requirements for the purposes of Condition GCSE(Electronics)2.3. Awarding organisations must comply with these requirements in relation to all GCSE Qualifications in Electronics they make available.

#### **Mathematical skills**

The subject content for GCSE Qualifications in Electronics is set out in the document published by the Secretary of State entitled 'Electronics GCSE subject content', document reference DFE-00039-2016 (the 'Content Document').

The Content Document sets out the mathematical skills and knowledge which must form part of each GCSE Qualification in Electronics ('Mathematical Skills') in the 'Use of Mathematics' sections, individual content statements and Appendix 3.

In designing and setting the Assessments by Examination for a GCSE Qualification in Electronics which it makes available, or proposes to make available, an awarding organisation must ensure that –

- questions and tasks rewarding the use of Mathematical Skills assess those skills within the context of other areas of the subject content, and not in isolation,
- (b) at least 20 per cent of the total marks for the qualification reward the use of Mathematical Skills at a Level of Demand which is not lower than that which is expected of Learners at Key Stage 3 as outlined in the Department for Education's document 'Mathematics programmes of study: key stage 3',<sup>6</sup> document reference DFE-00179-2013, and
- (c) without prejudice to the above requirements and those outlined in the Content Document, in each set of assessments Mathematical Skills are assessed across a range of Levels of Demand which supports effective differentiation in relation to the qualification.

<sup>&</sup>lt;sup>6</sup> <u>www.gov.uk/government/publications/national-curriculum-in-england-mathematics-programmes-of-</u> <u>study</u>

#### Non-examination assessment

Condition GCSE(Electronics)2.2(b) states that an awarding organisation must ensure that of the total marks available for a GCSE Qualification in Electronics, 20 per cent of those marks shall be made available through assessments which are not Assessments by Examination.

In respect of the assessments which are not Assessments by Examination, an awarding organisation must ensure that each Learner is required to complete an extended system design and construction task which –

- (a) assesses only assessment objective AO3,
- (b) allows the Learner to demonstrate the electronics skills specified in paragraph 10 of the Content Document,
- (c) requires the Learner to produce the following evidence -
  - (i) a design brief for an electronic system which has been developed in response to a problem<sup>7</sup>,
  - (ii) an electronic system based on that design brief, and
  - (iii) such additional evidence as is necessary to enable the consideration of that Learner's level of attainment in respect of all of the relevant criteria against which Learners' performance in that assessment will be differentiated, and
- (d) is taken under conditions specified by the awarding organisation, including, in particular, conditions which
  - (i) ensure that the evidence generated by each Learner can be Authenticated, and
  - (ii) require each Learner to produce the electronic system under Immediate Guidance or Supervision.

#### Setting of non-examination assessments

The problem which a Learner must address in an assessment which is not an Assessment by Examination may be set by –

(a) the awarding organisation,

<sup>&</sup>lt;sup>7</sup> Throughout this document, 'problem' should be interpreted consistently with the definition given in paragraph 10 of the *Electronics GCSE Subject Content* document.

- (b) the Centre which will deliver the assessment, or
- (c) the Learner, following discussion with the Centre which will deliver the assessment.

In any event, the awarding organisation must demonstrate to Ofqual's satisfaction in its assessment strategy that it has –

- (a) taken all reasonable steps to identify the risk of any Adverse Effect which may result from its approach to setting the assessment, and
- (b) where such a risk is identified, it has taken all reasonable steps to prevent that Adverse Effect or, where it cannot be prevented, to mitigate that Adverse Effect.

#### Marking of non-examination assessments

Evidence generated by a Learner in an assessment for a GCSE Qualification in Electronics which is not an Assessment by Examination may be marked –

- (a) by the awarding organisation or a person connected to the awarding organisation,
- (b) by a Centre, or
- (c) through a combination of (a) and (b).

In any event, the awarding organisation must demonstrate to Ofqual's satisfaction in its assessment strategy that –

- (a) it has taken all reasonable steps to identify the risk of any Adverse Effect which may result from its approach to marking the assessments (and to Moderation where appropriate) and
- (b) where such a risk is identified, it has taken all reasonable steps to prevent that Adverse Effect or, where it cannot be prevented, to mitigate that Adverse Effect.

# Subject content (published by Department for Education)



# **Electronics**

# **GCSE** subject content

February 2016

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# The content for GCSE electronics

## Introduction

1. The GCSE subject content sets out the knowledge, understanding and skills for GCSE specifications in electronics, to ensure progression from key stage 3 national curriculum science and mathematics requirements and the possibility of development into A level. They provide the framework within which awarding organisations create the detail of the subject specifications.

### Subject aims and outcomes

2. Specifications in GCSE electronics will ensure that students have the scientific and mathematical knowledge and understanding and engineering skills to tackle problems in an electronics context. GCSE electronics should be studied in such a way as to develop and maintain their interest in engineering subjects and the appreciation of their relevance to their everyday lives. The scope and nature of such study should be coherent and practical.

GCSE specifications in electronics should enable students to:

- develop scientific knowledge and conceptual understanding of the behaviour of analogue and digital electrical/electronic circuits including a wide range of electronic components
- develop an understanding of the nature, processes and methods of electronics as an engineering discipline to help them answer questions about practical circuits
- be aware of new and emerging technologies
- develop and learn how to apply observational, practical, problem-solving and evaluative skills in the identification of needs in the world around them and to propose and test electronic solutions
- progress to level 3 qualifications in electronics and engineering

### **Subject content**

3. Specifications for GCSE electronics should require students to develop the electronic engineering, knowledge, understanding and skills set out below.

4. Awarding organisations' specifications should be designed to set out the level of understanding which students are expected to acquire.

5. The knowledge and understanding section also sets out the mathematical skills and knowledge required for electronics. In order to be able to develop their skills, knowledge and understanding in electronics, students need to have been taught and demonstrate competence to select and apply the appropriate areas of mathematics as set out under each topic and the mathematical skills listed in appendix 3.

6. Three appendices provide further details about (1) equations in electronics;(2) units in electronics; and (3) mathematical skills.

#### Knowledge and understanding

7. GCSE specifications in electronics should reflect the aims and learning outcomes outlined above, and should include the knowledge, understanding and skills listed below, giving due consideration to the assessment objectives. The essential subject content outlined here provides the framework for developing a coherent study at GCSE.

8. This content sets out the full range of content for GCSE specifications in electronics. Awarding organisations may, however, use any flexibility to increase depth, breadth or context within the specified topics, as long as the rigour and challenge of the specification is maintained.

9. GCSE specifications in electronics should require students to demonstrate and apply knowledge and understanding of:

#### Scientific communication

- how to use scientific vocabulary, symbols, terminology and definitions
- how to use SI units

#### Use of apparatus

- how to connect electrical circuits
- how to take measurements on electrical circuits using multimeters (on voltage, current and resistance ranges), timing equipment, logic probes and oscilloscopes (or computers configured as oscilloscopes)

#### **Basic circuits**

- Electronic systems and sub-systems
  - the way in which electronic systems are assembled from sensing, processing and output sub-systems

- the principles of a range of sensing, processing and output sub-systems
- the need for and use of transducer drivers
- Circuit concepts
  - the communication of electrical circuits using standard circuit symbols connected in standard ways
  - how to analyse circuits in terms of voltage, current, resistance, energy and power
  - how to take measurements to test electrical components and circuits, including investigating current-voltage characteristics
- Resistive components in circuits
  - the qualitative and quantitative effects of resistors and resistor combinations (series and parallel) in circuits
  - the design and testing of sensing circuits using resistors and a range of passive sensors, including light, temperature, pressure, moisture, switch, sound and pulse generator
  - the selection of resistors for circuits, considering resistance, tolerance and power dissipation
- Switching circuits
  - how to analyse and compare the action of switching circuits based upon npn transistors, MOSFETs and voltage comparator ICs
  - how to perform calculations on switching circuits based upon npn transistors
  - the use of data to design switching circuits using npn transistors and comparators
- Applications of diodes
  - the current-voltage behaviour and uses of silicon diodes for component protection in DC circuits and half-wave rectification AC circuits
  - how zener diodes are used in voltage regulation circuits
- Uses of mathematics
  - the application of the equations relating pd, current, resistance, power, energy and time and solve problems for circuits which include resistors in series, resistors in parallel and resistors in sensing and transistor circuits

$$R = \frac{V}{I}; P = VI = I^2 R = \frac{V^2}{R}; E = Pt;$$

$$R = R_1 + R_2; \ R = \frac{R_1 R_2}{R_1 + R_2}; \ I_{\rm C} = h_{\rm FE} I_{\rm B}; \ V_{\rm OUT} = \frac{R_2}{R_1 + R_2} V_{\rm IN}$$

- calculate expected voltage values in order to test circuits
- plotting and interpreting I-V characteristic graphs and switching graphs

#### **Digital circuits**

- Combinational logic systems
  - the meaning of a logic level
  - the identification and behaviour of logic gates: NOT and 2-input AND, OR, NAND and NOR, singly and in combination
  - how to use truth tables and basic Boolean manipulation for systems of gates
  - how to design processing systems consisting of logic gates to solve problems, incorporating pull-up and pull-down resistors to provide the correct logic level
- Timing circuits
  - how the voltage across a charging or discharging capacitor in an RC circuit varies with time and the effect on the voltage-time graph of varying the values of R and/or C
  - how an RC network can be used to produce a time delay
  - the characterisation of monostable time-delay and astable circuits
  - how to configure a 555-timer as a monostable or an astable and to perform relevant calculations
  - how to use an oscilloscope (or a computer configured as an oscilloscope) to determine the properties of time delay and astable circuits
- Sequential systems
  - how to describe the action of rising-edge-triggered D-type flip-flops and latches (registers), BCD and decade counters
  - how to use timing diagrams for flip-flops, BCD counters and decade counters
  - how to construct 1- and 2-bit binary up-counters from D-type flip-flops
  - how to design and analyse systems using counters and combinational logic to produce a given sequence

- Uses of mathematics
  - logic simplification using  $\overline{A+B} = \overline{A} \cdot \overline{B}$ ;  $\overline{A \cdot B} = \overline{A} + \overline{B}$
  - the interpretation of decay graphs for *RC* networks, *V-t* graphs for monostables and astables and timing graphs for counters
  - the calculation of the time delay of a monostable and frequency, period, mark-space ratio for an astable

$$T = 1.1RC; f = \frac{1}{T} = \frac{1.44}{(R_1 + 2R_2)C}; \frac{T_{\text{ON}}}{T_{\text{OFF}}} = \frac{R_1 + R_2}{R_2}$$

#### Interfacing digital to analogue circuits

- Interface circuits
  - the action of a Schmitt inverter and how it can be used in debouncing mechanical switches and analogue sensors
  - the comparison of the properties of transistors, comparators and Schmitt inverters as interfaces between analogue and digital systems
  - how to design transistor switching circuits to interface input sensors to outputs

#### Analogue communications

- Operational amplifiers (op-amps)
  - how to define and calculate the voltage gain and bandwidth of an amplifier and the trade-off between them
  - how to draw circuits for non-inverting and inverting op-amp amplifier circuits and use of the gain formulae in calculations
  - how the output signal of an amplifier may be affected by clipping distortion
  - how to draw circuits for mixers based on a summing op-amp circuit and calculate the output voltage
- Uses of mathematics
  - calculations involving the gain of amplifiers:

$$G = \frac{V_{\text{OUT}}}{V_{\text{IN}}}; \ G = 1 + \frac{R_{\text{F}}}{R_{\text{I}}}; \ G = -\frac{R_{\text{F}}}{R_{\text{IN}}}; \ V_{\text{OUT}} = -R_{\text{F}}\left(\frac{V_{1}}{R_{1}} + \frac{V_{2}}{R_{2}} + \dots\right)$$

• how to use voltage gain-*f* graphs to determine bandwidth; *V*-*t* graphs to illustrate clipping distortion; calculations from  $V_{\text{IN}}$ - $V_{\text{OUT}}$  graphs

#### **Control circuits**

Microcontrollers

- the nature of a microcontroller as a programmable integrated circuit into which software can be loaded to carry out a range of different tasks
- how microcontrollers can be interfaced with sensing circuits and output devices
- how to design flowchart programs to enable microcontrollers to perform tasks

#### **Electronics skills**

10. Specifications for GCSE electronics should require students, in the context of the knowledge and understanding specified, to demonstrate the ability to:

- analyse a problem<sup>1</sup> to enable solutions to be developed
- develop a design brief to solve a problem
- propose an electronic system, composed of sub-systems, to satisfy a design brief
- make predictions about the way that electronic systems behave
- design and build an electronic system, modelling its performance against the design brief, modifying as appropriate
- plan tests to take measurements, to explore a problem, selecting appropriate techniques and instruments
- evaluate practical risks in system development and application
- carry out tests having due regard to the correct manipulation of apparatus, accuracy of measurement and Health and Safety considerations
- take and record measurements on electrical circuits
- report results using standard scientific conventions
- evaluate the performance of the electronic system against the design brief
- suggest improvements to the electronic system following evaluation

#### Assessment of electronics skills

11. Specifications for GCSE electronics should assess all these electronics skills by requiring students to undertake a single extended System Design and Construction task.

- context looking at situations for possible design openings e.g. people crossing the road
- opportunity possibilities arising e.g. from a new improved component

<sup>&</sup>lt;sup>1</sup> In the context of GCSE electronics skills, 'problem' is interpreted broadly, to embrace:

problem – difficulties in a situation e.g. people with partial hearing cannot hear the doorbell

### **Appendix 1**

#### **Equations in electronics**

(a) In solving quantitative problems, students should be able correctly to use the following relationships using standard SI units, without them being provided:

| potential difference = current × r      | esistance             | V = IR      |
|---|-----------------------|-------------|
| power = potential difference × cu       | ırrent                | P = VI      |
| power = $(current)^2 \times resistance$ |                       | $P = I^2 R$ |
| energy transfer = power × time          |                       | E = Pt      |
| $R = R_1 + R_2$                         | resistors in series   |             |
| $R = \frac{R_1 R_2}{R_1 + R_2}$         | resistors in parallel |             |

(b) In addition, students should be able correctly to select from a list and apply the following relationships

| $V_{\rm out} = \frac{R_2}{R_1 + R_2} V_{\rm in}$   | potential divider                         |
|--|---|
| $P = \frac{V^2}{R}$  | power dissipated in a resistor            |
| $I_{\rm C} = h_{\rm FE} I_{\rm B}$   | current gain of a junction transistor     |
| $f = \frac{1}{T}$  | frequency, period relationship            |
| T = 1.1RC  | time delay of a monostable                |
| $f = \frac{1.44}{\left(R_1 + 2R_2\right)C}$  | frequency of an astable                   |
| $\frac{T_{\rm ON}}{T_{\rm OFF}} = \frac{R_1 + R_2}{R_2}$   | mark/space ratio of an astable            |
| $G = \frac{V_{\text{OUT}}}{V_{\text{IN}}}$   | amplifier voltage gain                    |
| $G = 1 + \frac{R_{\rm F}}{R_{\rm I}}$  | non-inverting op-amp circuit voltage gain |
| $G = -\frac{R_{\rm F}}{R_{\rm IN}}$  | inverting op-amp circuit voltage gain     |
| $V_{\rm OUT} = -R_{\rm F} \left( \frac{V_1}{R_1} + \frac{V_2}{R_2} + \dots \right)$  | summing amplifier output voltage          |
| $\left. \begin{array}{c} \overline{A+B} = \overline{A} \cdot \overline{B} \\ \text{and} \\ \overline{A} \cdot \overline{B} = \overline{A} + \overline{B} \end{array} \right\}$ | Boolean identities                        |

## Appendix 2

#### SI units

Students should recognise, carry out calculations and be able to communicate using:

(a) The following SI units:

ampere (A), second (s), hertz (Hz), joule (J), watt (W), volt (V), ohm ( $\Omega$ );

(b) The following SI multipliers:

p, n, µ, m, k, M, G, T.

## **Appendix 3**

#### Mathematical skills required for GCSE electronics

#### 1. Arithmetic and numerical computation

- (a) Recognise and use expressions in decimal form
- (b) Recognise and use expressions in standard form
- (c) Use ratios, fractions and percentages
- (d) Calculate squares and square roots

#### 2. Handling data

- (a) Use an appropriate number of significant figures
- (b) Find arithmetic means
- (c) Make order of magnitude calculations

#### 3. Algebra

- (a) Understand and use the symbols =, <, <<, >>, >,  $\infty$ , ~
- (b) Change the subject of an equation
- (c) Substitute numerical values into algebraic equations using appropriate units for physical quantities
- (d) Solve simple algebraic equations
- (e) Use simple Boolean identities

#### 4. Graphs

- (a) Translate information between graphical and numeric form
- (b) Plot two variables from experimental or other data
- (c) Draw an appropriate trend line onto plotted data
- (d) Interpret data presented in graphical form
- (e) Determine the slope of a graph
- (f) Calculate the rate of change from a graph showing a linear relationship
- (g) Draw and use the slope of a tangent to a curve as a measure of rate of change



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